

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Yong CHEN, et al.

Confirmation No.: 8317

Application No.: 10/697,589

Examiner: Weiss, H.

Filing Date: 10/30/2003

Group Art Unit: 2814

Title: METHOD FOR MAKING NANOSCALE WIRES AND GAPS FOR SWITCHES AND TRANSISTORS

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment () Petition to extend time to respond
() New fee as calculated below () Supplemental Declaration
() No additional fee
() Other: _____ (fee \$ _____)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	6	MINUS	20	= 0	X \$50	\$ 0
INDEP. CLAIMS	1	MINUS	3	= 0	X \$200	\$ 0
[] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$360	\$ 0
EXTENSION FEE	1ST MONTH \$120.00	2ND MONTH \$450.00	3RD MONTH \$1020.00	4TH MONTH \$1590.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

Respectfully submitted,

Yong CHEN, et al.

By

John P. Wagner, Jr.

Attorney/Agent for Applicant(s)
Reg. No. 35,398

Date: 01/18/05

Date of Deposit: 01/18/05

Typed Name: Desiree Ortiz

Signature: 



ITZ

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)

Chen et al.)

Serial No.: 10/697,589)

Filed: October 30, 2003)

For: METHOD FOR MAKING)
NANOSCALE WIRES AND)
GAPS FOR SWITCHES AND)
TRANSISTORS)

Examiner: WEISS, H.

Art Unit: 2814

Confirmation No.: 8317

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENTS AND REMARKS

Dear Sir:

In response to the Office Action mailed October 15, 2004, please consider the following remarks and enter the following amendments.

Amendments to the specification are presented on page 2 of this response. Amendments to the claims are presented beginning on page 3 of this response. Remarks begin on page 5 of this response.